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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kiran Ganesh et al. Examiner: Phallaka Kik  
Serial No.: 09/431477 Group Art Unit: 2825  
Filed: November 1, 1999 Docket No: 884.141US1  
Title 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR  
VLSI DESIGN

**PETITION FOR A THREE-MONTH EXTENSION OF TIME**

Box AF  
Commissioner for Patents  
Washington, D.C. 20231

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a three-month extension of time be granted in which to respond to the Final Office Action mailed June 26, 2002, said period of response being extended from September 26, 2002 to December 26, 2002.

Please charge the required fee of 920.00 to Deposit Account No. 19-0743.

Respectfully Submitted

KIRAN GANESH ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date: December 26, 2002 By: Ann M. McCrackin  
Ann McCrackin  
Reg. No: 42,858

**CERTIFICATE UNDER 37 CFR § 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelop addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 26 day of December 2002

Name: Tina Kohut

Signature: [Signature]

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